

Docket No.: 43876-157

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Craig HANSEN, et al.	:	Confirmation Number: To be assigned
	:	
Serial No.: Unknown	:	Group Art Unit: To be assigned
	:	
Continuation of	:	
Application No. 10/646,787	:	
Filed August 25, 2003	:	
	:	
Filed: January 15, 2004	:	Examiner: To be assigned
	:	
For:	:	<u>PROGRAMMABLE PROCESSOR AND METHOD FOR PARTITIONED GROUP ELEMENT SELECTION OPERATION</u>

INFORMATION DISCLOSURE STATEMENT

Mail Stop NEW APPLICATIONS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Continuation of
Application No. 10/646,787
Filed August 25, 2003
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The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/646,787 , filed August 25, 2003 , which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: January 15, 2004

WDC99 864127-1.043876.0157

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 43876-157		SERIAL NO. Continuation of Serial No. 10/646,787		
			APPLICANT HANSEN, et al.				
			FILING DATE January 15, 2004		GROUP To be assigned		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	4,025,772	05/24/77	Constant				
	4,489,393	12/18/84	Kawahara, et al.				
	4,701,875	10/20/87	Konishi, et al.				
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987						
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen, IEEE Micro. April 1992, 40-63						
	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.						
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, Microprocessor Report July 31, 1995, 15-16						
EXAMINER			DATE CONSIDERED				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	0 474 246 A2	06/09/91	EP				
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	The Visual Instruction Set (VIS) in UltraSPAR™, L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, IEEE, May 3, 1995, 462-469						
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	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.						
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FOREIGN PATENT DOCUMENTS							
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